

● PRINTER RUSH ●
(PTO ASSISTANCE)

Application : 09527343 Examiner : Wilson GAU : 2661

From: ewc

Location: IDC FMF FDC

Date: 12/11/05

Tracking #: EP17709527343

Week Date: 9-19-05

~~ADM: Client Draftsman~~

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449		<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS		<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM		<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW		<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW		<input type="checkbox"/> Other
<input type="checkbox"/> DRW		
<input type="checkbox"/> OATH		
<input type="checkbox"/> 312		
<input checked="" type="checkbox"/> SPEC	<u>3-17-2000</u>	

[RUSH] MESSAGE:

*The serial number stamp is obscuring data
on pages 81, 82, 83, 84*

Thank you

[XRUSH] RESPONSE:

Fixed

INITIALS: *Rea*

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

REV 10/04

Table 15
ADDR=0x012: PM Register

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	--used--							LOS PM	OOF PM	B1 PM	B2 PM	AIS PM	REI PM	RDI PM	Inconsistent J0 PM	Mismatched J0 PM
Mode	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro
Default	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

5

- These bits are set if the corresponding failures occurred since the last 1 second tick.
- See GR-253 sections 6.2.2.3 and 6.2.2.4 for full descriptions of SONET/SDH PMs. Note that the J0 PMs are not defined in the SONET/SDH standards yet.

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Table 16
ADDR=0x013: B1 Error PM Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	Count															

15 • Contains the B1 error count.

Table 17
ADDR=0x014-0x15: B2 Error PM Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	unused															
Name	MSB															

20 • Contains the B2 error count.

Table 18
ADDR=0x016-0x17: REI_L PM Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	unused															
Name	MSB															

25 • Contains the REI_L count.

Validated Bytes

- If a new value is of K1/KS or S1 is found and consecutively repeated (3 times for K1/K2 and 8 times for S1) the new value will be stored in the Validated Registers for software to read.
- The first read of these registers will likely result in the actual validated values rather than the default value.

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Table 19
ADDR=0x018: Validated K1/K2 Value

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	Validated K1								Validated K2							
Mode	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro
Default	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

- Contains the validated K1 and K2 bytes.

5 •

ADDR=0x019: Validated S1 Value

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	unused								Validated S1							
Mode	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro	ro
Default	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

- Contains the validated S1 synchronization byte.

10

Table 21
ADDR=0x01B: Gigabit Ethernet Invalid Code Word PM Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	Count															

- Contains the number of GBE Invalid Code Words that were detected.

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Table 22
ADDR=0x01C: Gigabit Ethernet Disparity Error PM Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	Count															

- Contains the number of GBE Disparity Errors that were detected.

20

Table 23
ADDR=0x01D: Gigabit Ethernet Sync Loss PM Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	Count															

- This is a count of the transitions from in-sync to out-of-sync.

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Table 24
ADDR=0x01E: Combined Gigabit Ethernet PM Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	Count															

- The invalid-code and disparity errors are ORed together and the resultant error is counted.

Table 25
ADDR=0x030-0x031: Corrected Ones Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	unused														MSB	
															LSB	

5 • Contains the number of ones that were corrected by the FEC block.

Table 26
ADDR=0x032-0x033: Corrected Zeros Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	unused														MSB	
															LSB	

• Contains the number of zeros that were corrected by the FEC block.

15 **Table 27**
ADDR=0x034-0x035: Total Corrected Bits Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	unused														MSB	
															LSB	

• Contains the total number of bits that were corrected by the FEC block.

20 **Table 28**
ADDR=0x036-0x037: Total Corrected Bytes Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	unused														MSB	
															LSB	

• Contains the total number of bytes that were corrected by the FEC block.

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Table 29
ADDR=0x038-0x039: Uncorrectable 255-Byte Block Count

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	unused														MSB	
															LSB	

30 • Contains the total number of uncorrectable bytes that were detected by the FEC block.

Table 30
ADDR=0x080-0x09F: Expected Section Trace Message

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
080																
081																
...																
09f																
	First byte of message								Second byte of message							
	Third byte of message								Fourth byte of message							
							
	63rd byte of message								64th byte of message							
Default	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

5 • Contains the expected section trace message that was programmed via software.

Table 31
ADDR=0x0A0-0x0BF: Received Section Trace Message

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0a0																
0a1																
...																
0bf																
	First byte of message								Second byte of message							
	Third byte of message								Fourth byte of message							
							
	63rd byte of message								64th byte of message							
Default	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

10 • If there is a mismatch interrupt showing, this memory will contain the mismatched message.
 • If there are no errors showing, this memory will show the expected message (unless a power-up reset has just occurred).
 • If there is an inconsistent interrupt showing, this memory will contain the last valid message, or, if no valid messages have been received since the last reset, random data.

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20 **Table 32**
ADDR=0x0C0-0x0DF: Transmit Section Trace Message

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0c0																
0c1																
...																
0df																
	First byte of message								Second byte of message							
	Third byte of message								Fourth byte of message							
							
	63rd byte of message								64th byte of message							
Default	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

• Contains the section trace message to be transmitted that was programmed via software.

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